

1 Standard characteristics

Standard characteristics described below are just examples of the 3850 Group (spec. A)'s characteristics and are not guaranteed. For rated values, refer to "3.1 Electrical characteristics" of Datasheet.

1.1 Flash memory version power source current standard characteristics

Figure. 1 to Figure. 7 show flash memory version (M38507F8A) power source current standard characteristics.

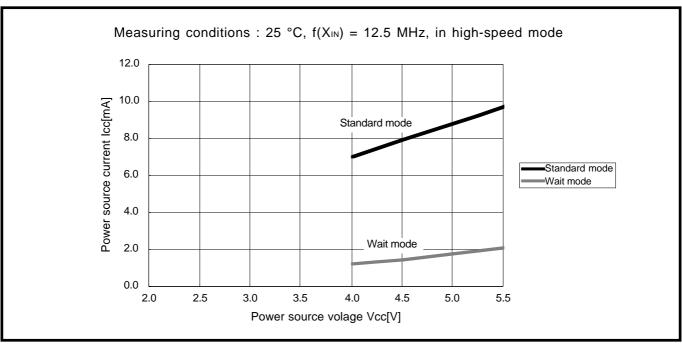


Fig. 1 Flash memory version power source current standard characteristics (in high-speed mode, $f(X_{IN}) = 12.5 \text{ MHz}$)

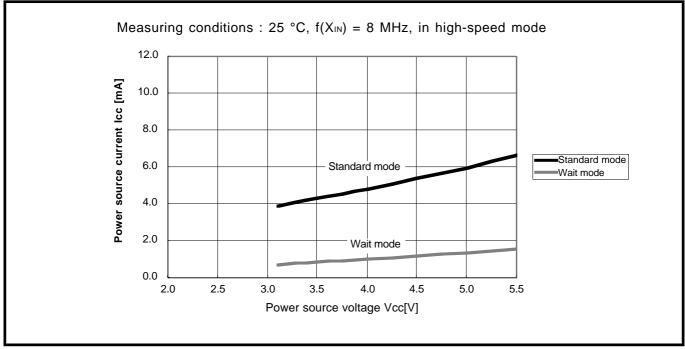


Fig. 2 Flash memory version power source current standard characteristics (in high-speed mode, $f(X_{IN}) = 8 \text{ MHz}$)



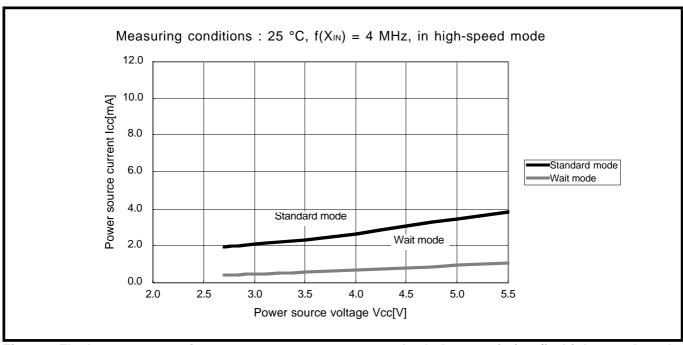


Fig. 3 Flash memory version power source current standard characteristics (in high-speed mode, $f(X_{IN}) = 4 \text{ MHz}$)

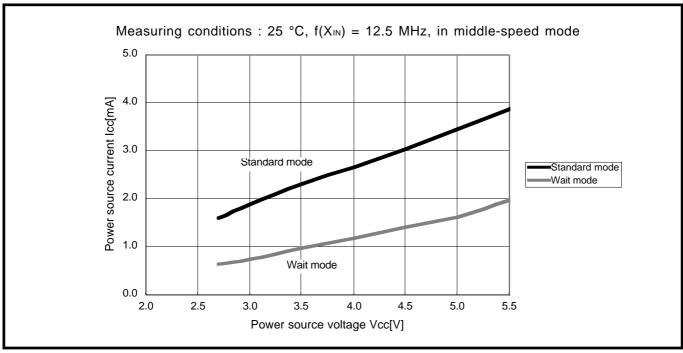


Fig. 4 Flash memory version power source current standard characteristics (in middle-speed mode, $f(X_{IN}) = 12.5 \text{ MHz}$)



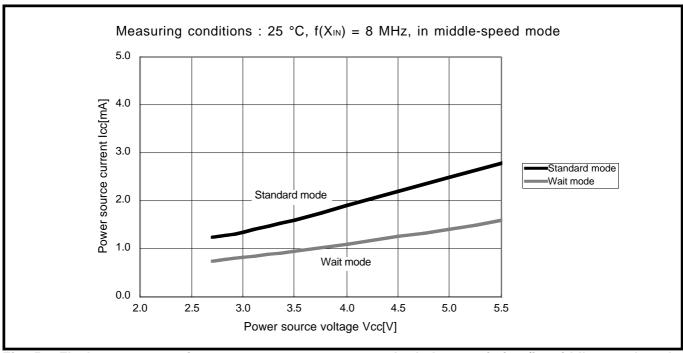


Fig. 5 Flash memory version power source current standard characteristics (in middle-speed mode, $f(X_{IN}) = 8 \text{ MHz}$)

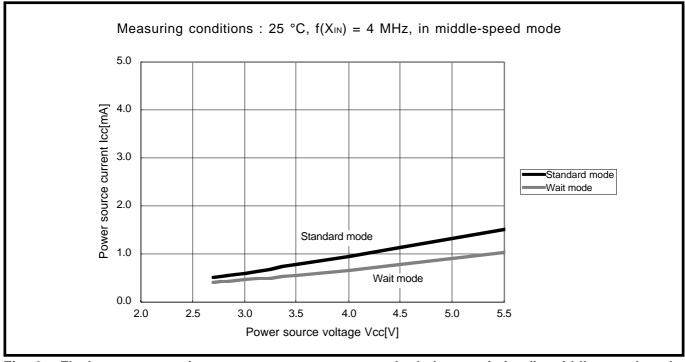


Fig. 6 Flash memory version power source current standard characteristics (in middle-speed mode, $f(X_{IN}) = 4 \text{ MHz}$)



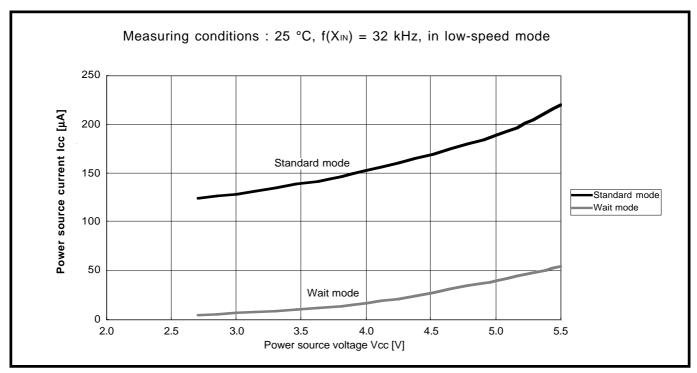


Fig. 7 Flash memory version power source current standard characteristics (in low-speed mode)



1.2 Mask ROM version power source current standard characteristics

Figure. 8 – Figure. 14 show mask ROM version (M38503M2A, M38503M4A, M38504M6A, M38507M8A) power source current standard characteristics.

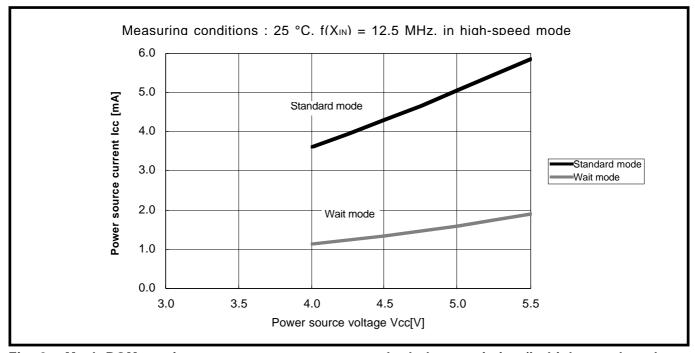


Fig. 8 Mask ROM version power source current standard characteristics (in high-speed mode, $f(X_{IN}) = 12.5 \text{ MHz}$)

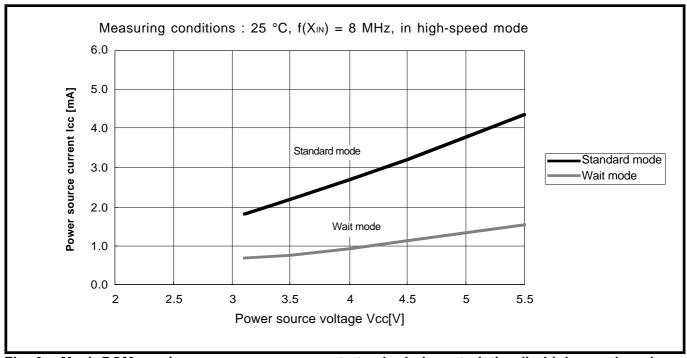


Fig. 9 Mask ROM version power source current standard characteristics (in high-speed mode, $f(X_{IN}) = 8 \text{ MHz}$)



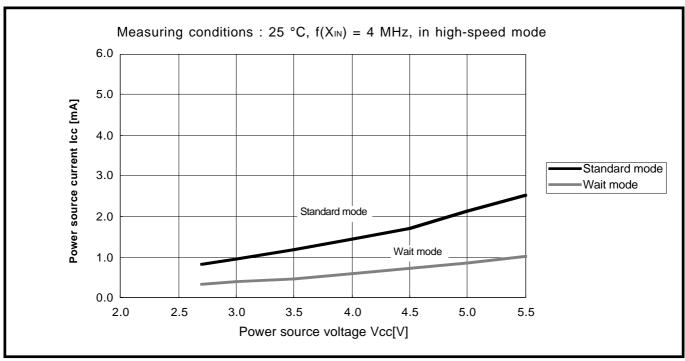


Fig. 10 Mask ROM version power source current standard characteristics (in high-speed mode, $f(X_{IN}) = 4 \text{ MHz}$)

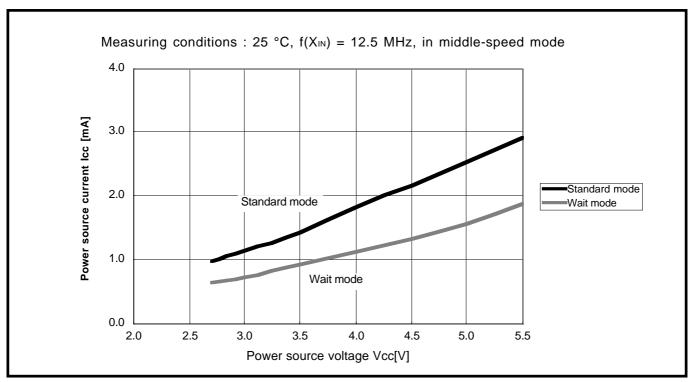


Fig. 11 Mask ROM version power source current standard characteristics (in middle-speed mode, $f(X_{IN}) = 12.5 \text{ MHz}$)



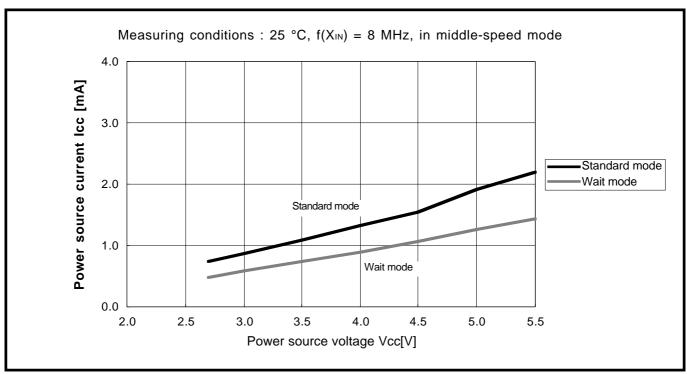


Fig. 12 Mask ROM version power source current standard characteristics (in middle-speed mode, $f(X_{IN}) = 8 \text{ MHz}$)

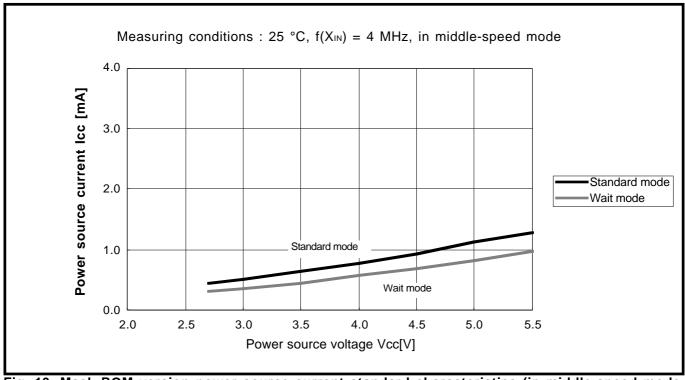


Fig. 13 Mask ROM version power source current standard characteristics (in middle-speed mode, $f(X_{IN}) = 4 \text{ MHz}$)



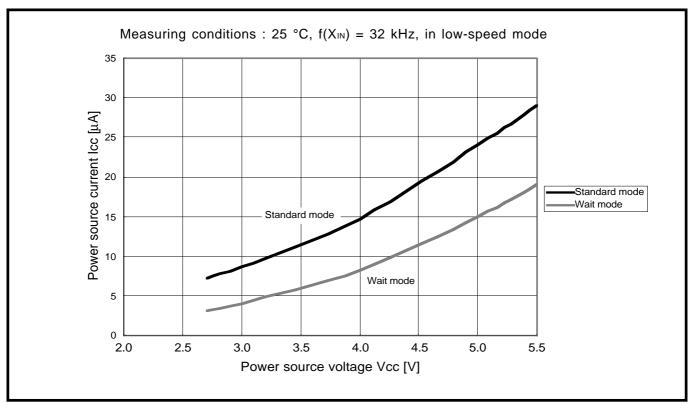


Fig. 14 Mask ROM version power source current standard characteristics (in low-speed mode)



1.3 Flash memory version port standard characteristics

Figure. 15, Figure. 16, Figure. 17 and Figure. 18 show flash memory version (M38507F8A) port standard characteristics.

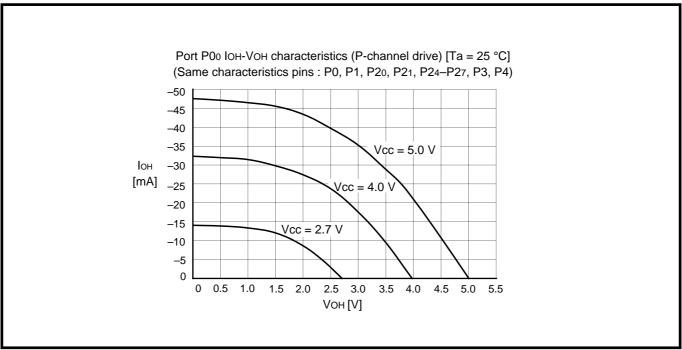


Fig. 15 CMOS output port P-channel side characteristics (Ta = 25 °C)

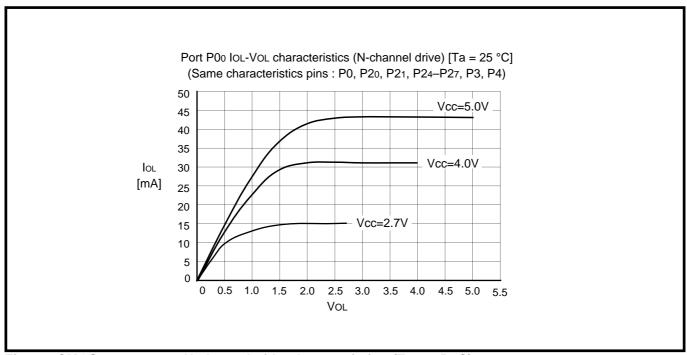


Fig. 16 CMOS output port N-channel side characteristics (Ta = 25 °C)



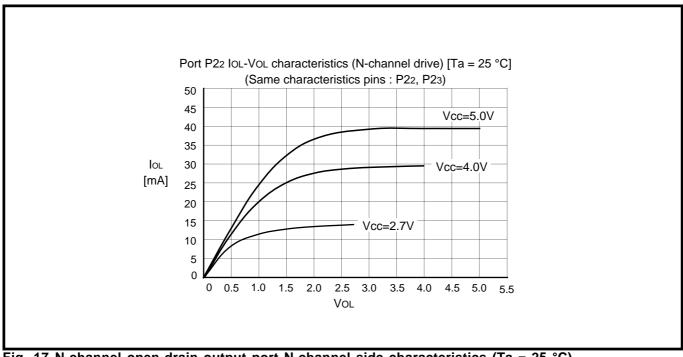


Fig. 17 N-channel open-drain output port N-channel side characteristics (Ta = 25 °C)

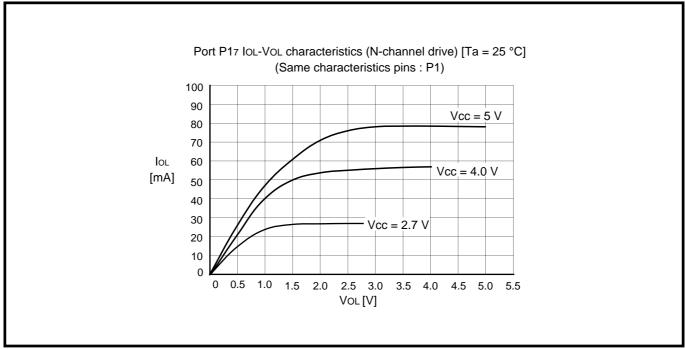


Fig. 18 CMOS large current output port N-channel side characteristics (Ta = 25 °C)



1.4 Mask ROM version port standard characteristics

Figure. 19, Figure. 20, Figure. 21 and Figure. 22 show mask ROM version (M38503M2HA, M38503M4HA, M38504M6A, M38507M8A) port standard characteristics.

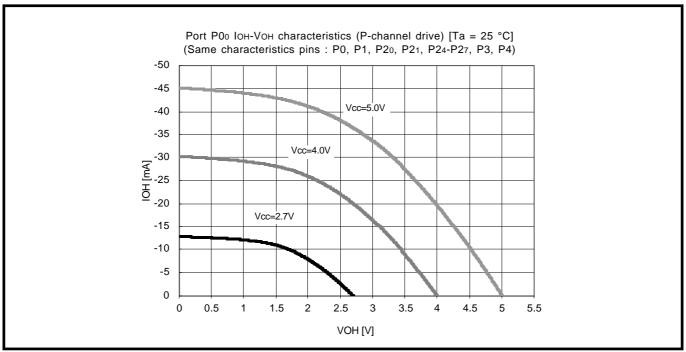


Fig. 19 CMOS output port P-channel side characteristics (Ta = 25 °C)

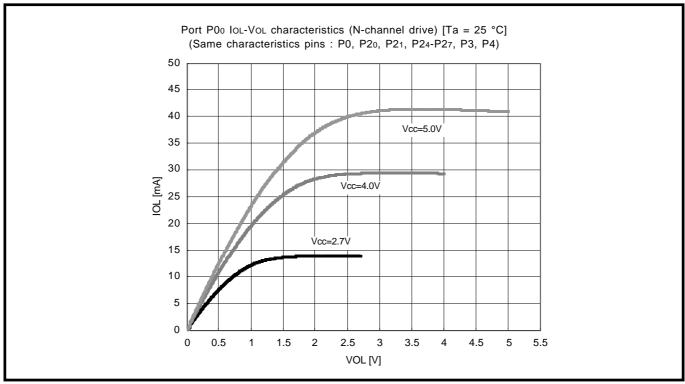


Fig. 20 CMOS output port N-channel side characteristics (Ta = 25 °C)



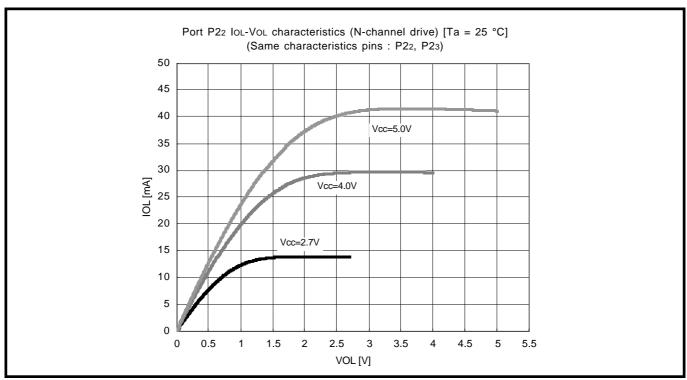


Fig. 21 N-channel open-drain output port N-channel side characteristics (Ta = 25 °C)

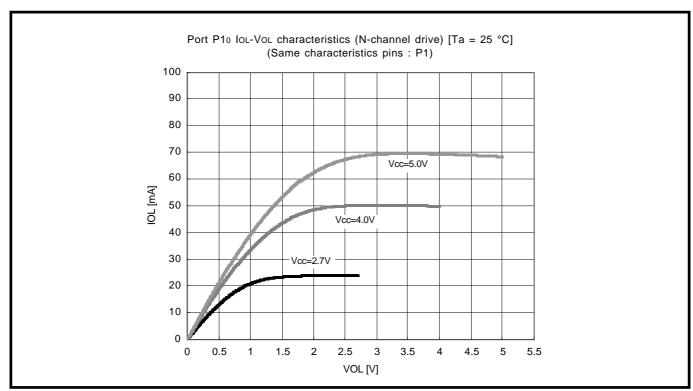


Fig. 22 CMOS large current output port N-channel side characteristics (Ta = 25 °C)



1.5 A-D conversion standard characteristics

(1) Definition of A-D conversion accuracy

The A-D conversion accuracy is defined below.

● Relative accuracy

① Zero transition voltage (V₀τ)

This means an analog input voltage when the actual A-D conversion output data changes from "0" to "1"

② Full-scale transition voltage (V_{FST})

This means an analog input voltage when the actual A-D conversion output data changes from "1023" to "1022".

3 Linearity error

This means a deviation from the lone between V_{0T} and V_{FST} of a converted value between V_{0T} and V_{FST} .

Differential non-linearity error

This means a deviation from the input potential difference required to change a converted value between V_{0T} and V_{FST} by 1 LSB of the 1 LSB at the relative accuracy.

Absolute accuracy

This means a deviation from the ideal characteristics between 0 to VREF of actual A-D conversion characteristics.

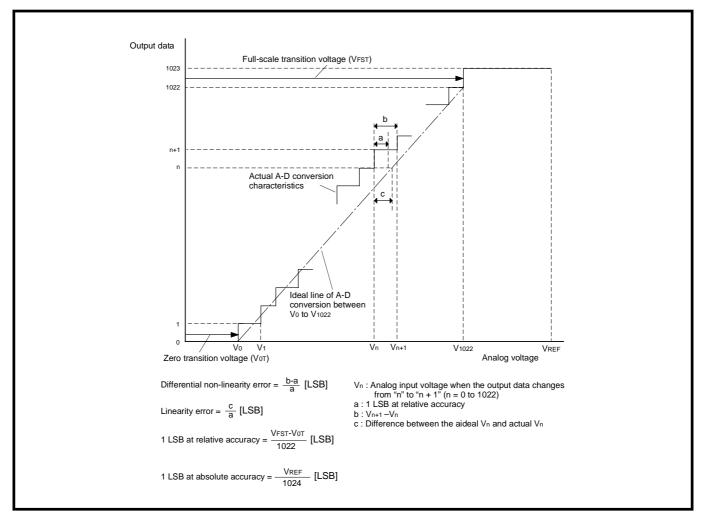


Fig. 23 Definition of A-D conversion accuracy



(2) A-D conversion standard characteristics

Figure. 24 – Figure. 27 show the A-D conversion standard characteristics of flash memory version, mask ROM version, and PROM version, respectively.

The thick lines of the graph indicate the absolute precision errors, These are expressed as the deviation from the ideal value when the output code changes. For example, the change in output code from 256 to 257 should occur at 1280 mV, but the measured value is 2.5 mV. Accordingly, the measured point of change is 1280 + 2.5 = 1282.5 mV.

The thin lines of the graph indicate the input voltage width for which the output code is constant. For example, the measured input voltage width for which the output code is 256 is 5.0 mV, so that the differential non-linear error is 5.0 - 5.0 = 0 mV (0 LSB).



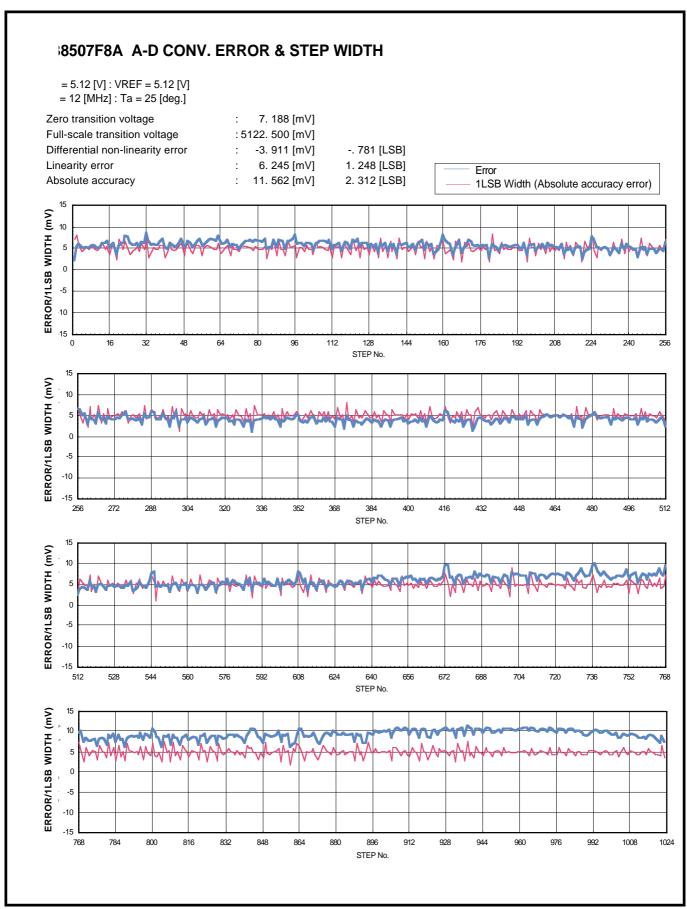


Fig. 24 Flash memory version (M38507F8A) A-D conversion standard characteristics (XIN=12MHz)



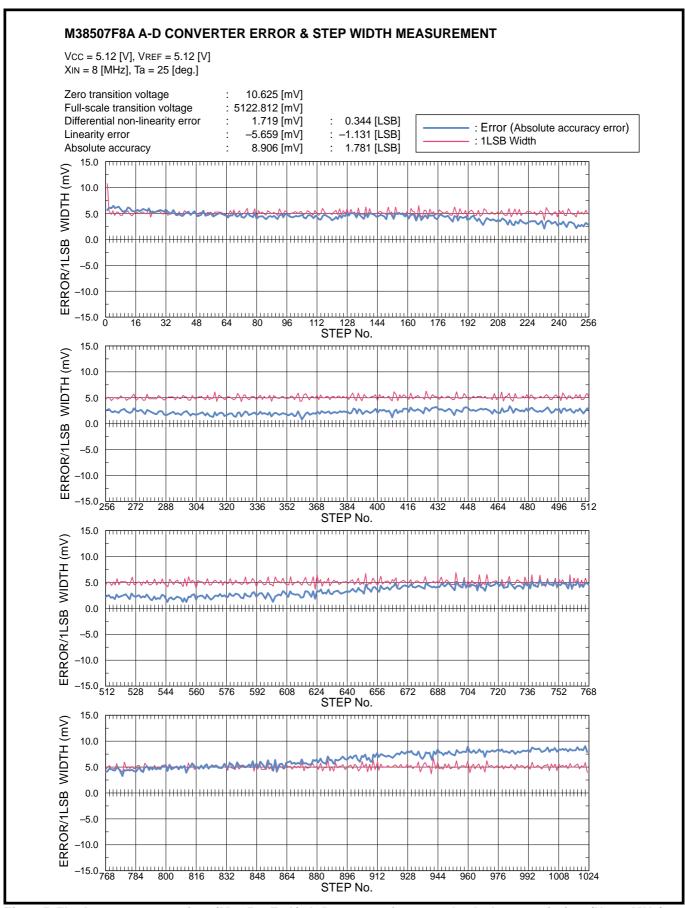


Fig. 25 Flash memory version (M38507F8A) A-D conversion standard characteristics (XIN=8MHz)



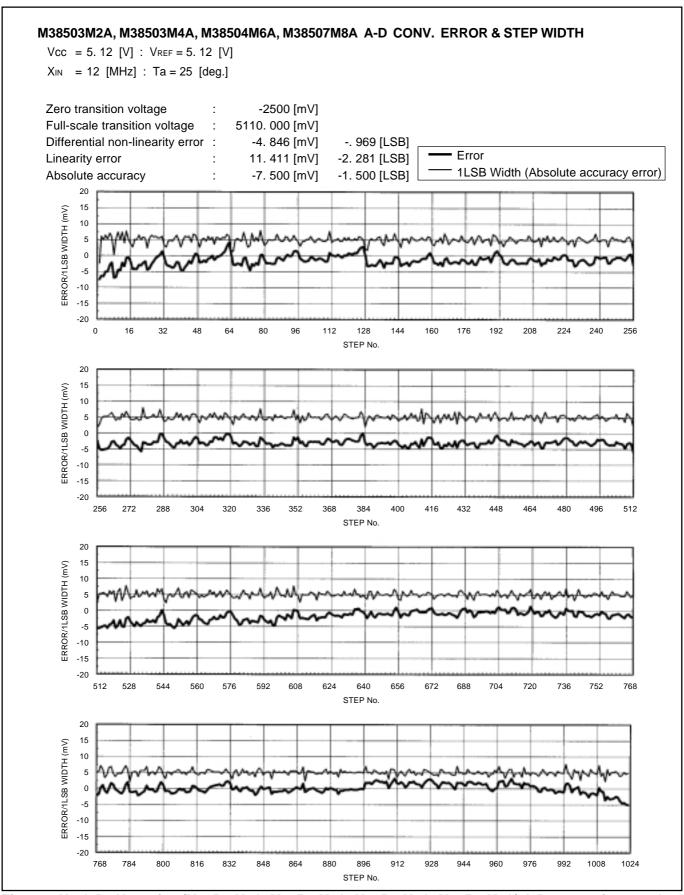


Fig. 26 Mask ROM version (M38503M2A, M38503M4A, M38504M6A, M38507M8A) A-D conversion standard characteristics (XIN=12MHz)



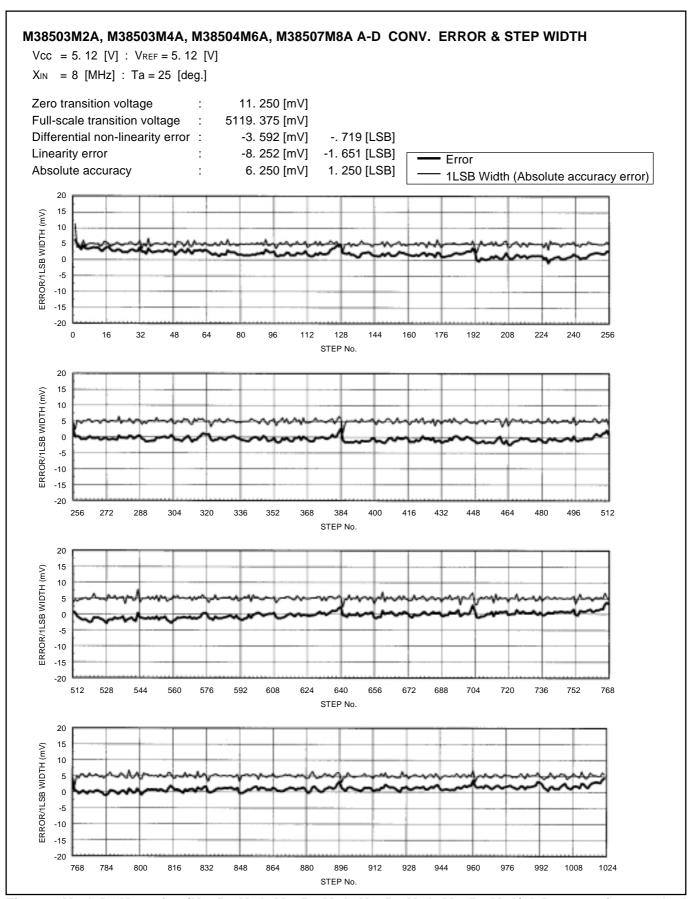


Fig. 27 Mask ROM version (M38503M2A, M38503M4A, M38504M6A, M38507M8A) A-D conversion standard characteristics (XIN=8MHz)

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